

FIG. 2

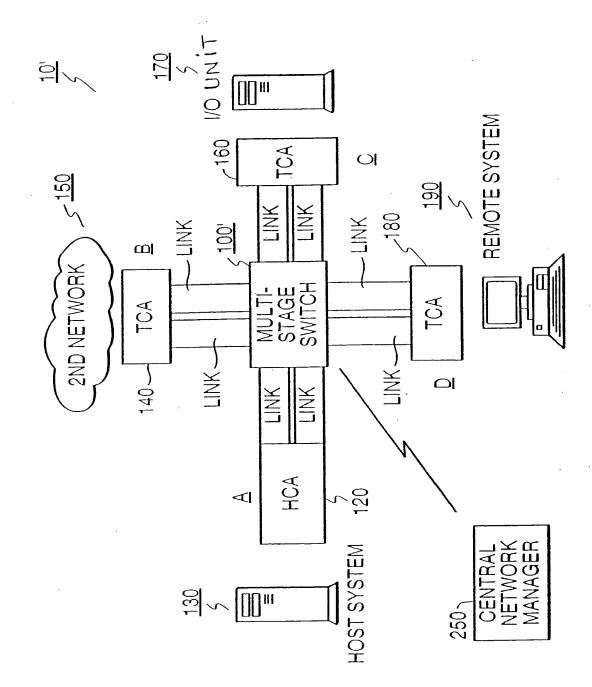
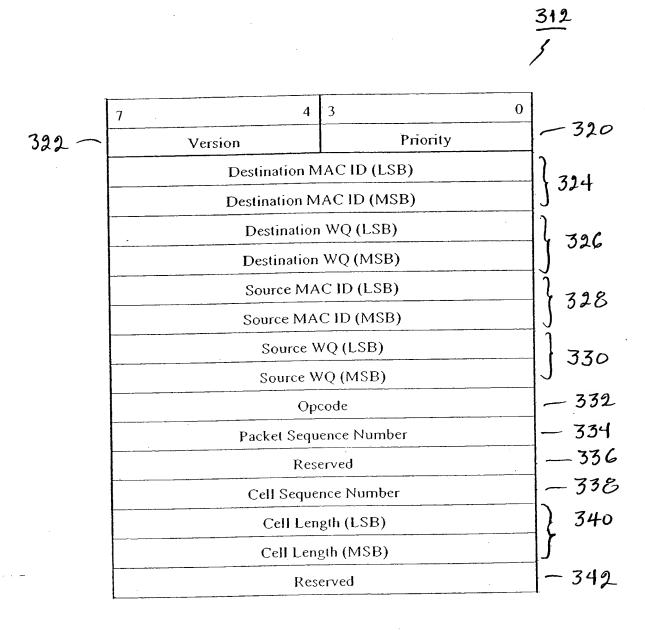


FIG. 3B



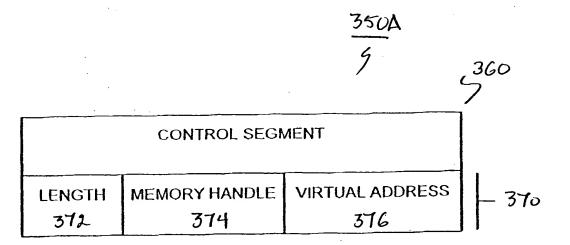


FIG. 3C

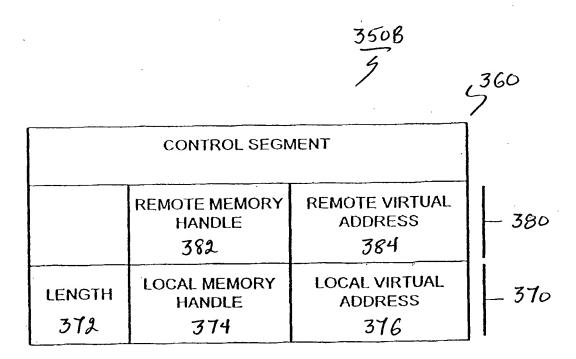
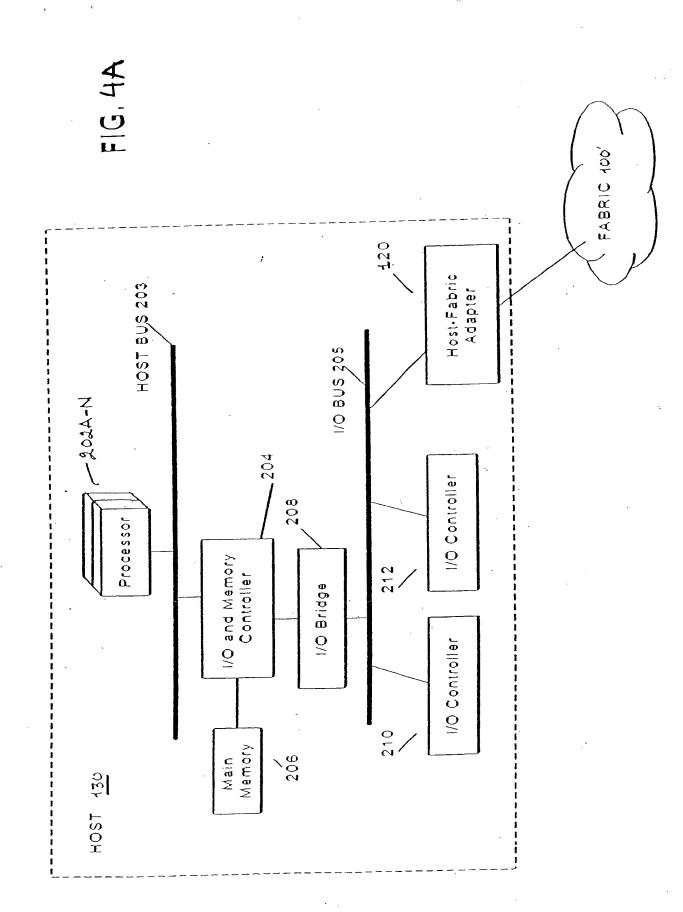
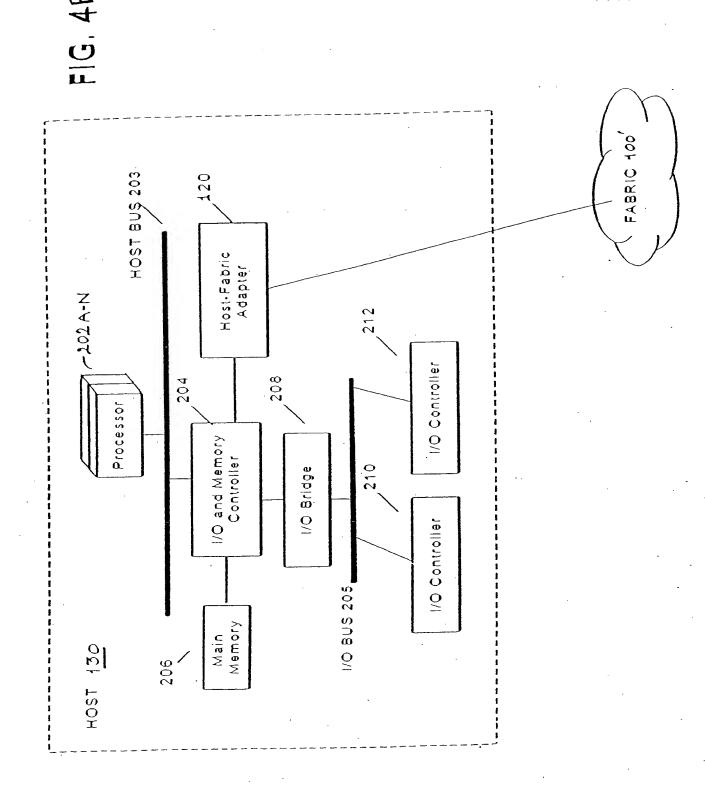
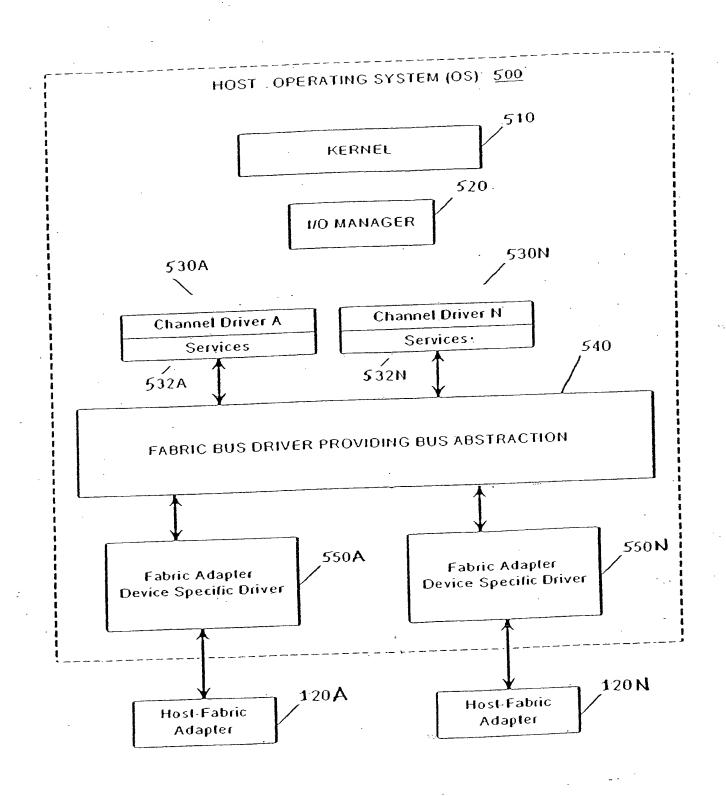


FIG. 3D







EXAMPLE SOFTWARE DRIVER STACKS OF HOST SYSTEM

FIG. 5

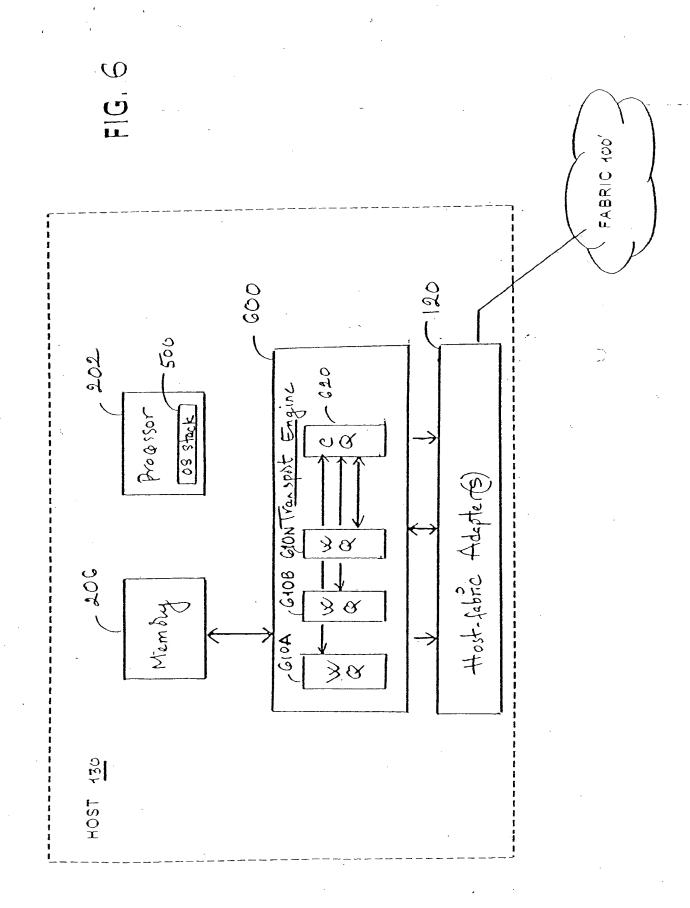


FIG.7

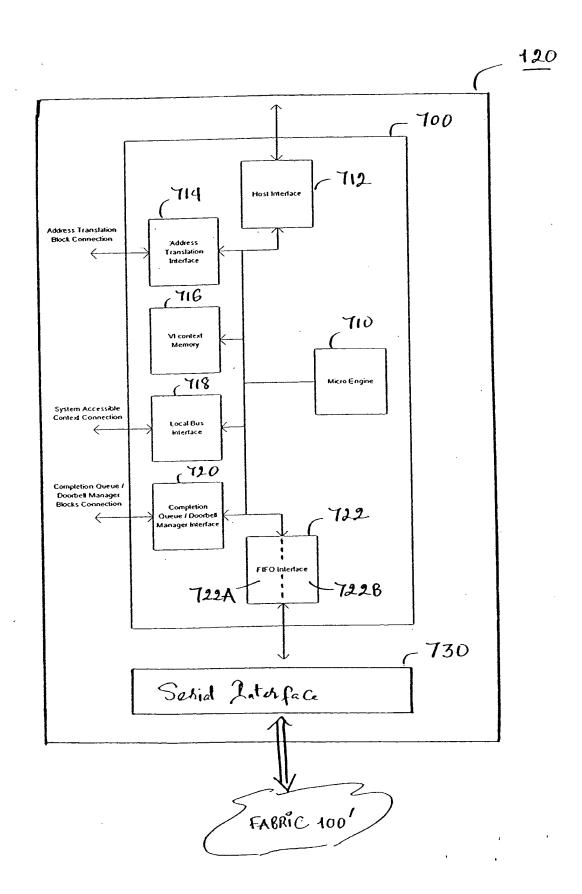
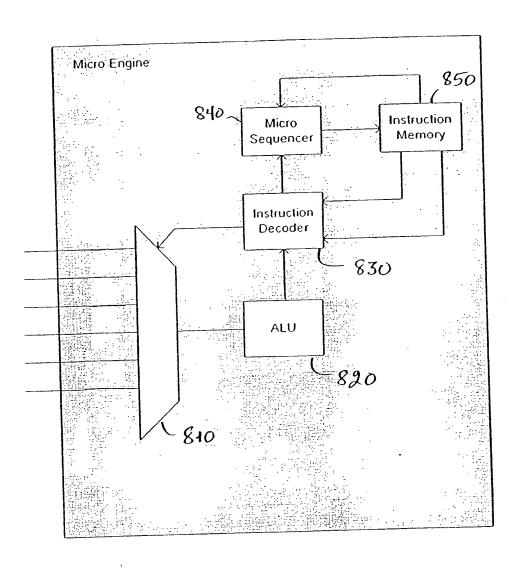


FIG. 8





51~

for I C steadsorm 810 998 2:1 Mux Branch Offset or Absolute Address ... olts[21:10] Instruction 850 Instruction 830 ALU Instruction Pointer 1840 Flag Register 7 8 12 Micro Sequencer 418 B Bus 1810A A Bus 8108 ο χ Σ Σ

FIG. 10

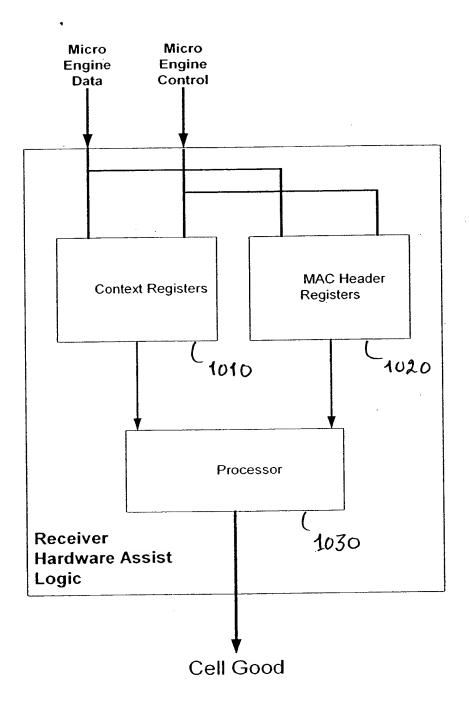


FIG. 11

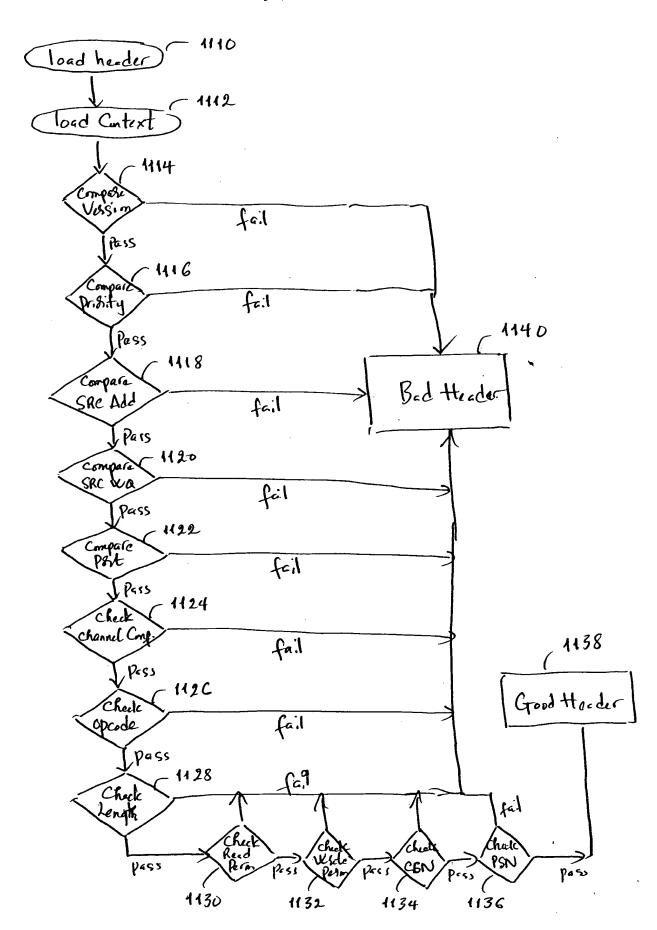
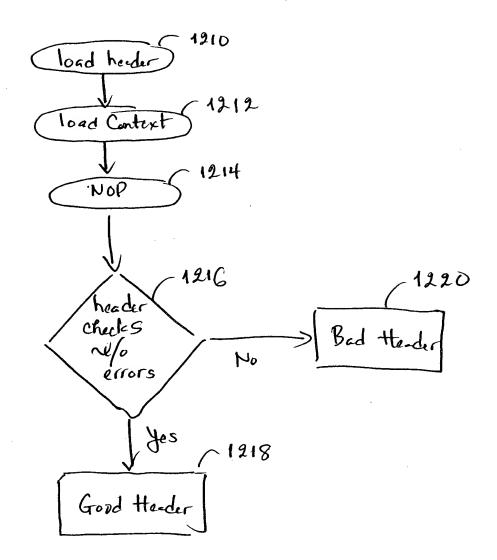
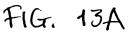
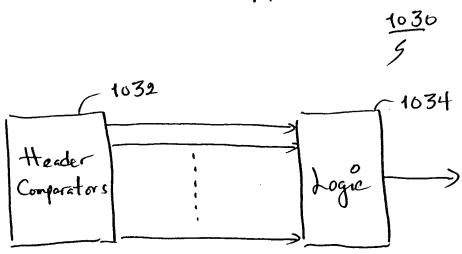


FIG. 12







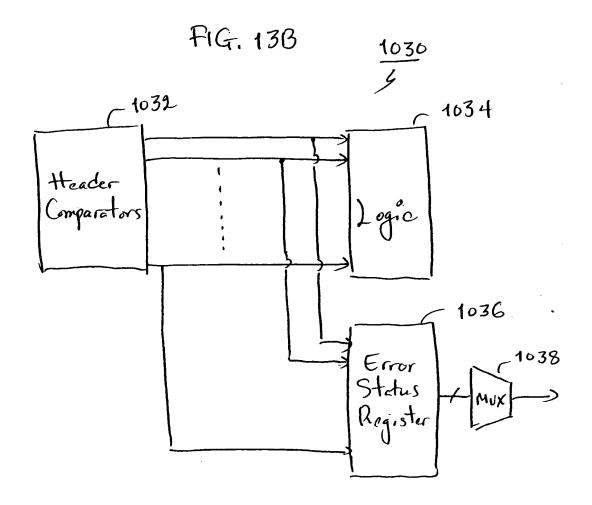
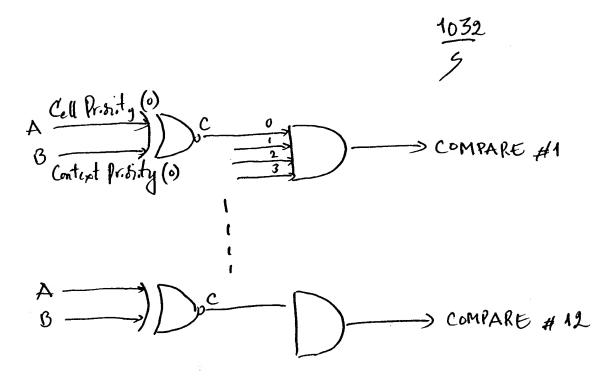


FIG. 14A



F1G. 14B

X	Nol	2	1032
A	B	C	
0	O.	1	
0	1	0	
1	0	0	
1	1	1	

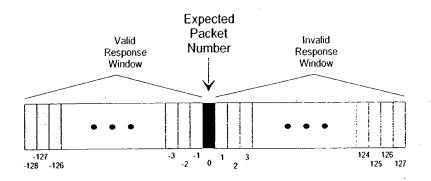


Figure 15 Definition of Earlier and Later Packets

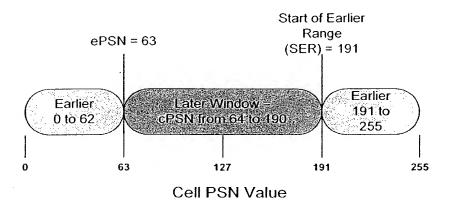


Figure 16 Later Window without Wrap

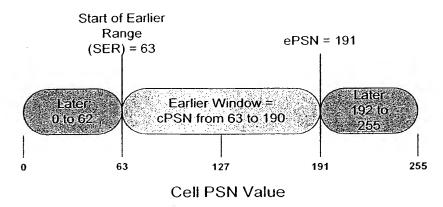


Figure 17 Earlier Window without Wrap

FIG. 18

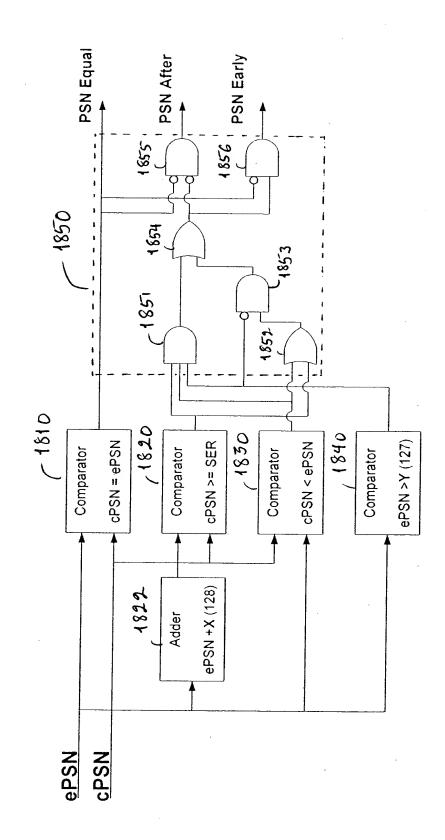


FIG. 19

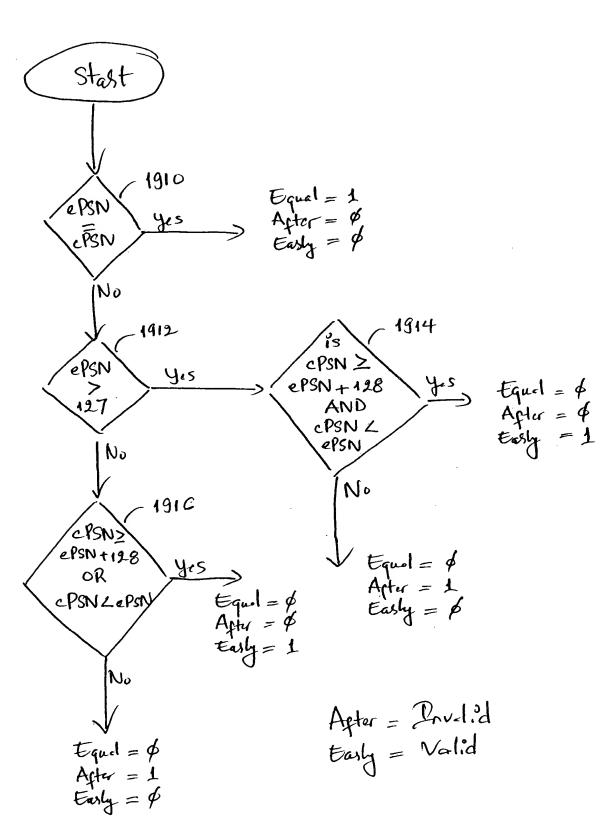


FIG. 20

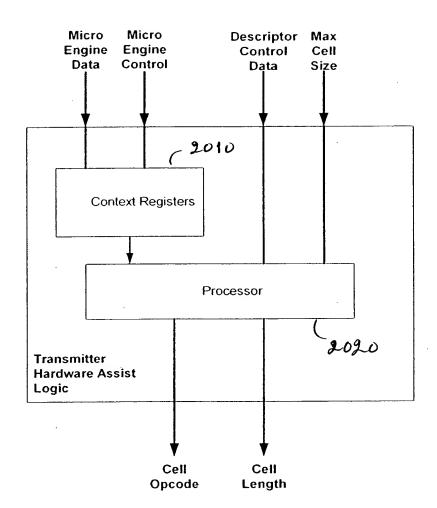
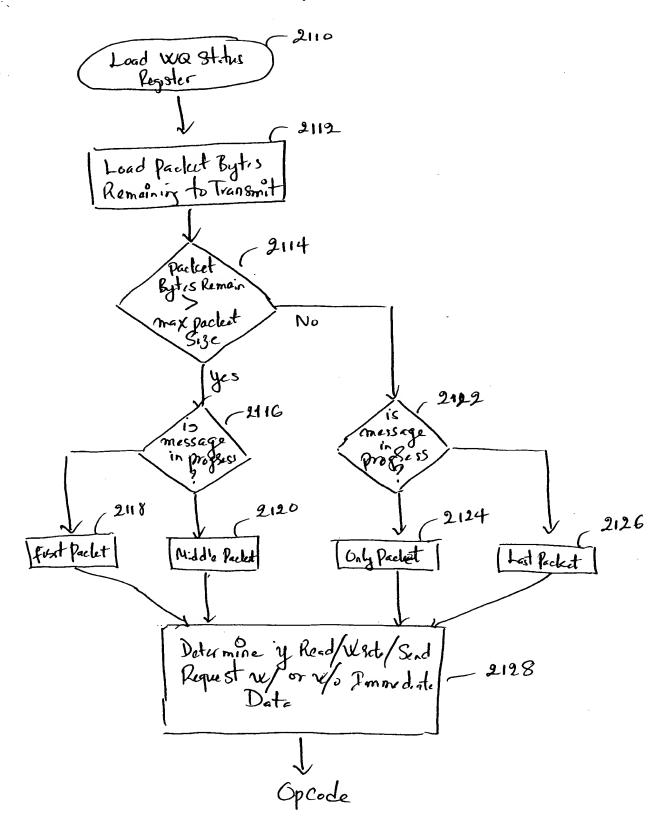


FIG. 21



F16.22

